



**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Application of:**

Yongjun Hu

**Serial No.:** 10/771,828

**Filed:** February 4, 2004

**For:** PROCESS FOR FORMING A  
DIFFUSION BARRIER MATERIAL  
NITRIDE FILM

**Confirmation No.:** 3960

**Examiner:** Q Hoang

**Group Art Unit:** 2818

**Attorney Docket No.:** 2269-6983.5US  
(96-0790.05/US)

**NOTICE OF EXPRESS MAILING**

Express Mail Mailing Label Number: EL994845935US

Date of Deposit with USPS: September 12, 2005

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**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

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P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In compliance with the duty to disclose information material to patentability pursuant to 37 C.F.R. § 1.56, it is respectfully requested that this Supplemental Information Disclosure Statement be entered and the document listed on attached Form PTO-1449 or PTO/SB/08 be considered by the Examiner and made of record. A copy of the listed document is enclosed pursuant to 37 C.F.R. § 1.98(a).

In accordance with 37 C.F.R. § 1.97(g) and (h), filing of this Supplemental Information Disclosure Statement is not to be construed as a representation that a search has been made or an

**Serial No. «Patent Application No»**

admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b). Further, no representation is made by Applicant herein that no other possible material information as defined in 37 C.F.R. § 1.56 (b) exists.

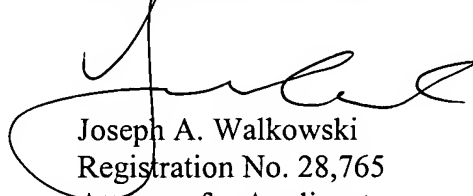
Other Documents

WOLF, S., "Silicon Processing for the VLSI Era," Vol. 2, Lattice Press, 1990, pp. 147-150.

Applicant offers to supply any explanation or discussion of the document which the Examiner feels is necessary or desirable and which is requested.

This Supplemental Information Disclosure Statement is filed concurrently with an RCE in the above-identified application, and therefore no additional fee is due.

Respectfully submitted,



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Date: September 12, 2005

JAW/dlm:slm

Enclosures: Form PTO-1449 or PTO/SB/08

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PTO/SB/08B(10-01)

Approved for use through 10/31/2002. OMB 0651-0031

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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet

1

of

1

**Complete if Known**

Application Number

10/771,828

Filing Date

February 4, 2004

First Named Inventor

Yongjun Hu

Group Art Unit

2818

Examiner Name

Q Hoang

Attorney Docket Number

2269-6983 5US (96-0790 05/US)

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		WOLF, S., "Silicon Processing for the VLSI Era," Vol. 2, Lattice Press, 1990, pp. 147-150.	

Examiner  
SignatureDate  
Considered

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Unique citation designation number (optional). <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.

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